U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Docket Number 10191/4116

Application Number 10/524,610

August 11, 2005

Examiner HO, Hoang Quan Tran Art Unit 9981

Invention Title

Inventor(s)

LAYER SYSTEM HAVING A SILICON LAYER AND A PASSIVATION LAYER, METHOD FOR CREATING A PASSIVATION LAYER ON A SILICON LAYER AND ITS USE LAERMER et al.

Address to:
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Commissioner for Patents
P.O. Box 1450
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Sir:

- 1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following reference to the attention of the Examiner. The reference is listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- 2. A copy of the reference listed on the modified PTO form 1449 is enclosed, unless otherwise indicated.
- 3. It is hereby certified that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Supplemental Information Disclosure Statement.
- 4. The Commissioner is hereby authorized to charge payment of the 37 C.F.R. § 1.117(p) {submission of an information disclosure statement under § 1.97(c)} fee of \$180.00 by credit card and any additional fees that may be required to the deposit account of Kenyon & Kenyon, deposit account number 11-0600.

Respectfully submitted,

Date: September 5, 2008

By: /Gerard A. Messina/

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